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**APPLICANTS**

Masaki Ito, Machida, JAPAN;

**\*\* CONTINUING DATA \*\*\*\*\***This application is a CON of 10/083,341 02/27/2002 PAT 6,708,322 VERIFIED**\*\* FOREIGN APPLICATIONS \*\*\*\*\***JAPAN 2001-170636 06/06/2001 VERIFIED**IF REQUIRED, FOREIGN FILING LICENSE GRANTED****\*\* 05/01/2004**

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature _____ Initials _____	STATE OR  COUNTRY JAPAN	SHEETS  DRAWING 14	TOTAL  CLAIMS 5	INDEPENDENT  CLAIMS 2
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**ADDRESS**

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**TITLE**

Integrated circuit, integrated circuit design method and hardware description generation method to generate hardware behavior description of integrated circuit

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